

The European Chips Act

WBG-Pilot Line:

Pilot line on advanced semiconductor devices based on wide bandgap materials

Boosting Europe's innovation & competitiveness in power electronics

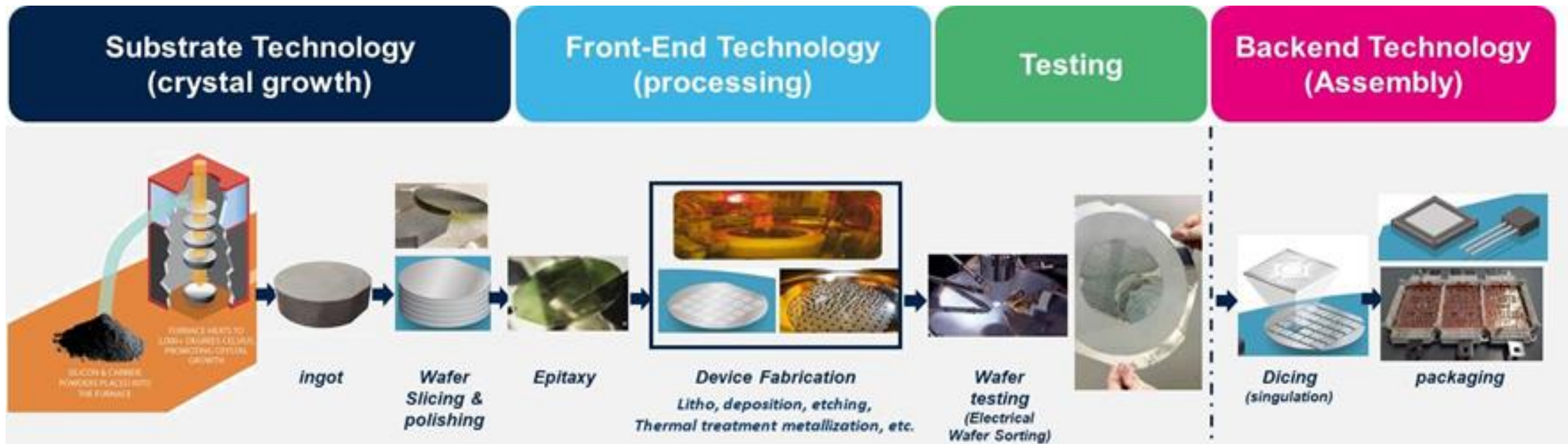
Jörg Schulze,

Fraunhofer Institut für Integrierte Systeme und Bauelementetechnologie - IISB, Erlangen, Germany

Vision of the WBG-Pilot line

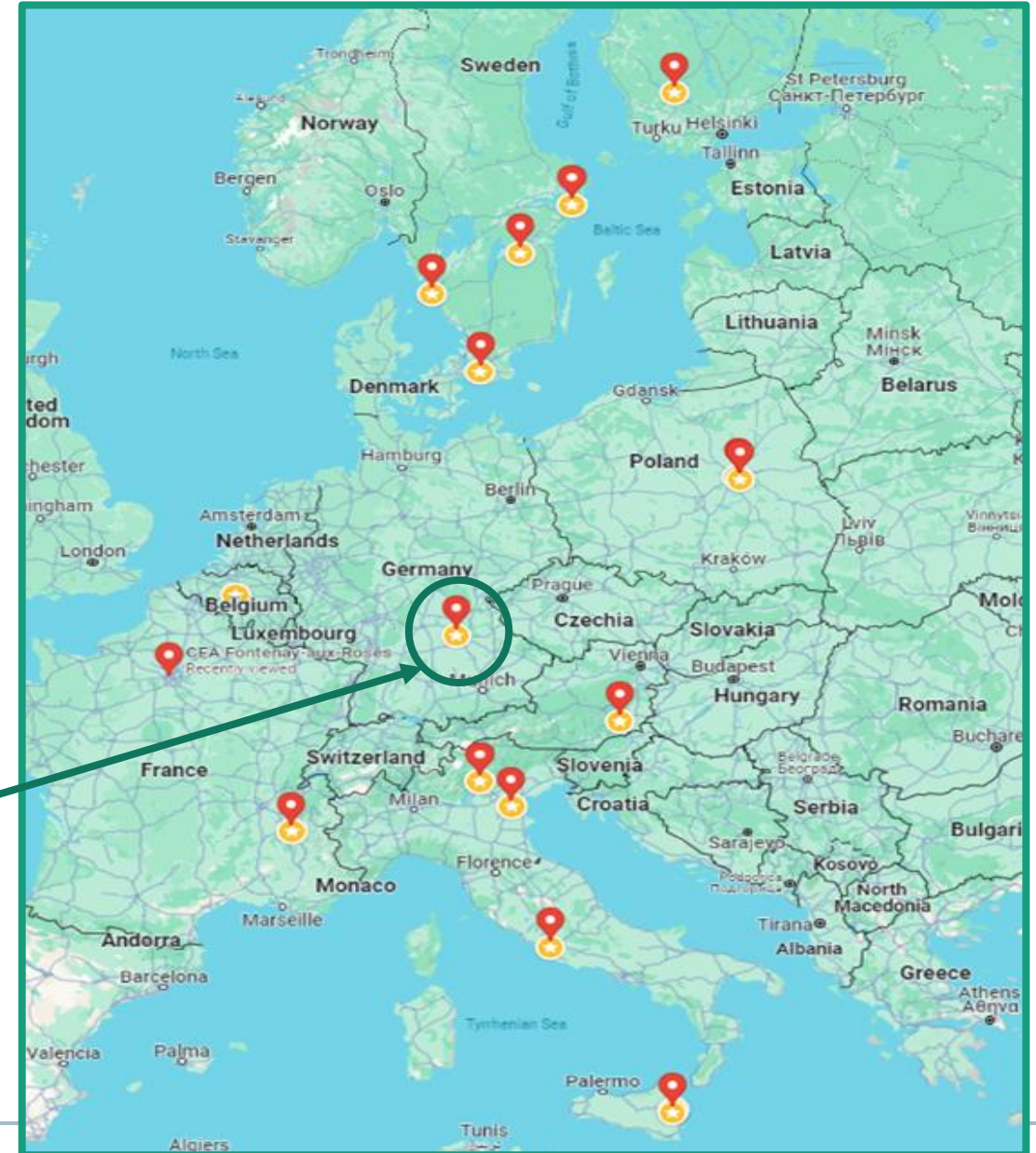
The R&D activities aim to:

- improve the **efficiency** and **power density** of **WBG-based** power devices
- cover the entire value chain, from crystal growth, front-end & testing, back-end technology



General idea, vision and topical coverage of the WBG Pilot Line

Participant No. *	Participant organisation name	Country
1 (Coordinator)	CNR	Italy
2	FBK	Italy
3	IU.NET	Italy
4	CHIPS-IT	Italy
5	KTH	Sweden
6	LIU	Sweden
7	ULUND	Sweden
8	CHALMERS	Sweden
9	Łukasiewicz - IMiF	Poland
10	IHPP PAS	Poland
11	TAU	Finland
12	SAL Austria	Austria
13	CEA LETI	France
14	Fraunhofer-IISB	Germany

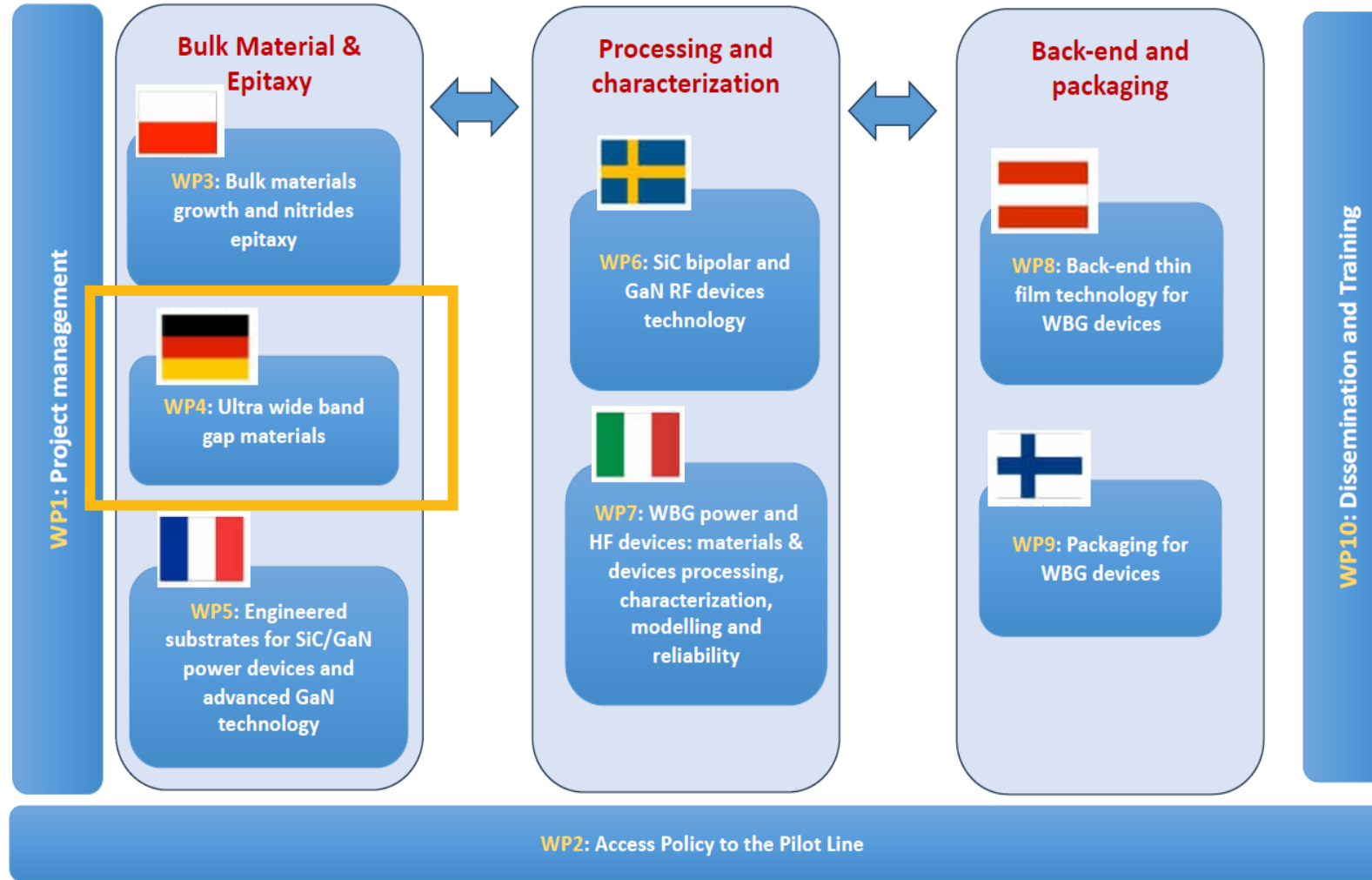


Distributed pilot line across Europe

- 7 Nations
- 14 Partner institutions

WBG Pilot Line

Overview and general concept after workpackages



Coordinator:

Francesco La Via,
Consiglio Nazionale Delle
Ricerche (CNR), **Italy**

German node -> FhG-IISB:

- Exploration of process options for small or irregular-sized, novel UWBG wafers
- Offer UWBG expertize from crystal growth to technology in Europe
- Solidify UWBG expertise in Germany, explicitly AIN
- Excellerate Lab2Fab transition

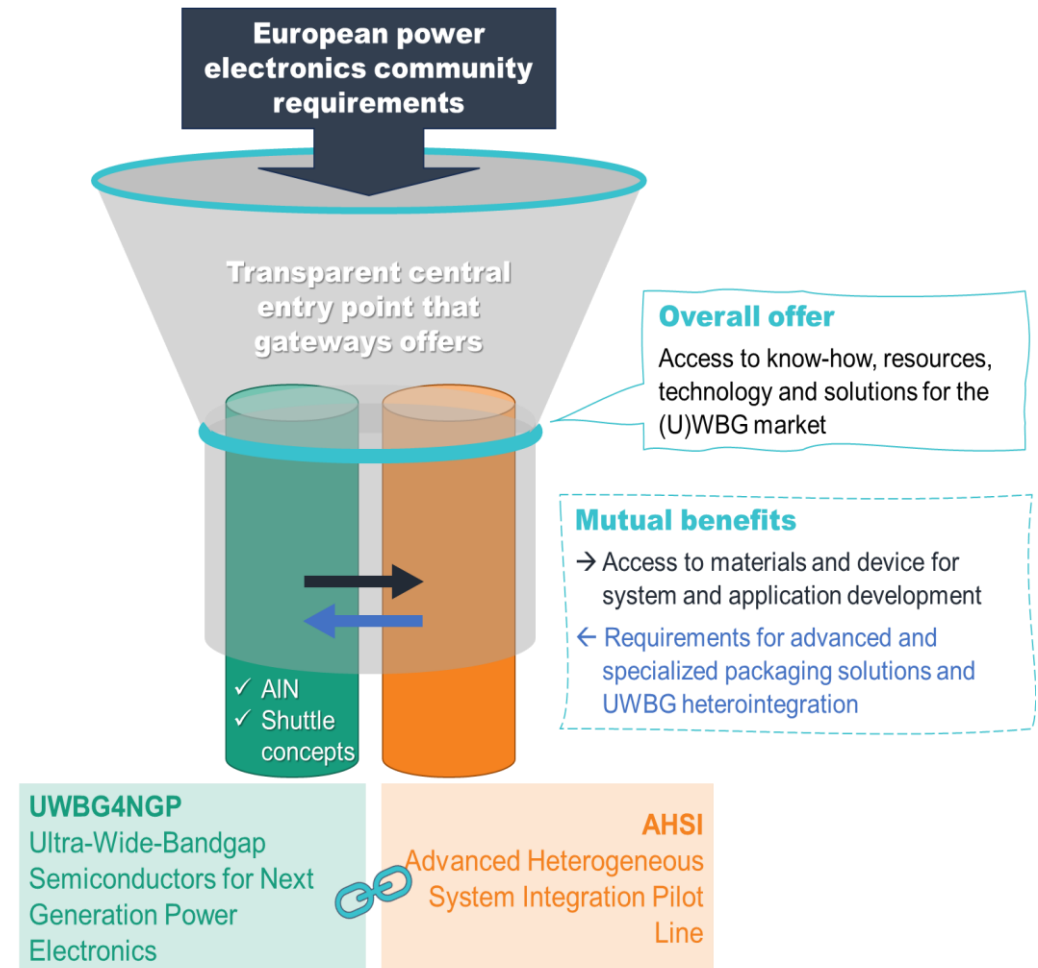
WBG Pilot Line

Concept for the German contribution by FhG-IISB

Ultrawide band gap semiconductors for next generation power devices

Lab2Fab initiative
for a faster bridging of the „valley of death“

The **goal of the WBG Pilot Line** is to significantly expand the competitiveness of the European semiconductors industry by strengthening the entire value chain and enabling a fast adoption of advanced (U)WBG technologies to high-value applications.





Contact

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